

DATE: 28/08/2019

Event Coordinator(s)

1. Prof. Jyotsna More
2. Prof. Kunal Meher

Student Coordinator(s)

1. Shrishti Karkera
2. Bhumi Tejani
3. Himanshu Gharat
4. Abhiruchi Patil
5. Janhvi Mathkar

Time & Place:

Computer Center at 3:30
pm

Department: CSX

No of participant: 47

Description

- This workshop was conducted for FEs and SEs, aiming at introducing students to the structural basics and functionality of a computer system.
- The speakers covered the following topics during the workshop:
 - Disassembling the units of the cabinet.
 - Detail study of the cabinet.
 - Study of the motherboard.
 - Uses and purpose of each.
 - Brief introduction of the family currently viewing (the CPU).
 - Assembling the units back.
- After the explanation, students were allowed to examine the CPU samples by themselves and doubts were solved simultaneously.
- Towards the end, the speakers encouraged the students learn more about the topics discussed during the workshop through different means like the internet and the books.
- It was also suggested to learn about detailed comparison between different architectures of microprocessor of different families.
- Overall, the workshop was a great success and from the feedback received, majority of the students found it to be very useful. The speakers were also appreciated by the attendees.

Resource Persons:

- Mr. Anand Pierre, IT
- Abhishek Sharma (TE IT)

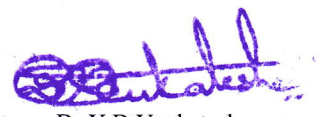
The event was conducted in the computer center at 3:30 on 28th August, 2019.



Prof. Jyotsna More
CSX Staff Convener



Prof. Kunal Meher
CSX Staff President



Dr. Y.D. Venkatesh
Principal

